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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Yvon GRIS
Serial No: Unassigned
Confirmation No: Unassigned
Filed: Herewith
For: METHOD FOR FABRICATING AN NPN TRANSISTOR
IN A BICMOS TECHNOLOGY

Examiner: Unassigned
Art Unit: Unassigned

Mail Stop Patent Application

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

PRELIMINARY AMENDMENT

Prior to examination, please amend this application as follows:

IN THE SPECIFICATION

Please insert the following paragraph on page 1 after the title of the invention:

Cross Reference To Related Applications

This application is a continuation of prior application serial number 10/436,352, filed May 12, 2003, which in turn is a continuation of prior application serial number 10/289,170, filed November 6, 2002, which in turn is a continuation of prior application serial number 10/109/622, filed March 28, 2002, which in turn is a continuation of prior application serial number 09/988,385, filed November 19, 2001, which in turn is a continuation of prior application serial number 09/873,795, filed June 4, 2001, which in turn is a continuation of prior application serial number 09/696/633, filed October 25, 2000, which in turn is a continuation of prior application serial number 08/969,800, filed November 13, 1997, entitled METHOD FOR

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FABRICATING AN NPN TRANSISTOR IN A BICMOS TECHNOLOGY, which applications
are incorporated herein by reference.